

## AMD VERSAL™

### VERSAL™ ARCHITECTURE, NOC AND METHODOLOGY

TRAINING	PRICE	SESSIONS
A_QSTART Quick Start : Designing with AMD Versal™ Adaptive SoC (French Language) 9h - 17h CET (1 day)	0 €	• June 6, 2024 EN LIGNE
ACAP_ARC Designing with the Adaptive SoC Versal™ : Architecture and Methodology (4 days)	3400 €	• October 21, 2024 EN LIGNE

### VERSAL™ AI ENGINE

TRAINING	PRICE	SESSIONS
AIE_INTR Workshop : Designing with AMD Versal™ AI Engine: Quick Start (French Language) 9h - 17h CET (1 day)	0 €	• March 29, 2024 EN LIGNE
ACAP_AIE Designing with Versal™ AI Engine (4 days)	3400 €	• September 23, 2024 EN LIGNE

## AMD VITIS™

### SOFTWARE DESIGN OF EMBEDDED SYSTEMS WITH THE VITIS TOOL

TRAINING	PRICE	SESSIONS
E_VITIS Software design of embedded systems with the Vitis™ tool (2 days)	2000 €	

### ACCELERATING APPLICATIONS WITH THE VITIS

TRAINING	PRICE	SESSIONS
AI_ACCEL Accelerating Applications with the Vitis™ Unified Environment Software (3 days)	2700 €	• April 22, 2024 EN LIGNE

### HIGH-LEVEL SYNTHESIS WITH THE VITIS HLS TOOL

TRAINING	PRICE	SESSIONS
D_HLS Vitis™ High Level Synthesis (2 days)	2000 €	

## AMD SOCS

### EMBEDDED DESIGN FOR AMD SOCS

TRAINING	PRICE	SESSIONS
E_ZAHS The essentials of embedded design for Xilinx Zynq™-7000, Zynq™ MPSoC and Versal™ components (4 days)	3400 €	• October 7, 2024 EN LIGNE

### MPSOC ZYNQ™ ULTRASCALE+

TRAINING	PRICE	SESSIONS
E_ZUPAHS Zynq UltraScale+™ MPSoC : System Architecture, Hardware and Software Design (4 days)	3400 €	• September 9, 2024 EN LIGNE

### PETALINUX™

TRAINING	PRICE	SESSIONS
E_PLNX Embedded Design with AMD PetaLinux Tools (3 days)	2700 €	• March 11, 2024 EN LIGNE

## AMD FPGA

### AMD VIVADO™

TRAINING	PRICE	SESSIONS
F_VBASE Designing FPGAs Using the Vivado™ (4 days)	3400 €	• December 2, 2024 EN LIGNE
F_STAXDC Static Timing Analysis (STA), Xilinx Design Constraints (XDC) and Advanced use of Vivado™ (4 days)	3400 €	• May 21, 2024 EN LIGNE
F_DFX Designing with Dynamic Function eXchange (DFX) Using the Vivado™ (3 days)	2700 €	• November 19, 2024 EN LIGNE

### ARCHITECTURE ADVANCED TRAINING

TRAINING	PRICE	SESSIONS
F_US Designing with the AMD UltraScale™ and UltraScale+™ Families (2 days)	2000 €	• June 4, 2024 EN LIGNE
F_7SERIE Designing with the AMD 7-Series Families (2 days)	On site and/or according to requests	

## DIGITAL SIGNAL PROCESSING ON RFSOC AND FPGA

### DESIGNING WITH THE ZYNQ ULTRASCALE+ RFSOC

TRAINING	PRICE	SESSIONS
C_RFSOC Design with the Zynq™ UltraScale+ RFSoc (3 days)		On site and/or according to requests

### DSP FUNCTIONS

TRAINING	PRICE	SESSIONS
D_ESS Essential DSP implementation techniques for AMD FPGAs (2 days)		On site and/or according to requests

## CONNECTIVITY

### MULTI-GIGABIT TRANSCEIVERS

TRAINING	PRICE	SESSIONS
C_TRX Designing with Serial Transceivers (2 days)		On site and/or according to requests

### PCI EXPRESS

TRAINING	PRICE	SESSIONS
C_PCIE Designing an Integrated PCI Express System (2 days)		On site and/or according to requests

## HDL LANGUAGES

### VHDL

TRAINING	PRICE	SESSIONS
L_VHDL VHDL Logical Synthesis and Simulation for AMD FPGA design (5 days)	4000 €	• November 4, 2024 EN LIGNE

### UPGRADING VHDL TO VERILOG AND VERILOG TO VHDL

TRAINING	PRICE	SESSIONS
L_VV_UP Upgrading VHDL to Verilog and Verilog to VHDL (2 days)	2000 €	