

# Workshop : Designing with AMD Versal™ AI Engine: Quick Start (French Language) 9h - 17h CET

1 day - 7 hours

## OBJECTIVES

- During the event, you will be introduced to the new AMD Xilinx Versal ACAP platform and its building blocks that enable flexible implementation of accelerated systems. The specific features of AI engines and their implementation will be discussed. The objective is to enable you to identify the use of AI engines in your new Versal applications.

## PREREQUISITES

- Comfort with the C/C++ programming language
- Software development flow
- Vitis software for application acceleration development flow

## CONCERNED PUBLIC

- Technicians and Engineers in Digital Electronics
- All our training courses are given at a distance and are accessible to people with reduced mobility.
- Our partner AGEFIPH accompanies us to implement the necessary adaptations related to your disability.



## NOTES

- Release date: 13/02/2023

## CHAPTERS

### DAY 1

- Versal ACAP Architecture Overview {Lecture}

- Introduction to the Versal AI engine architecture {Lecture}
- Versal AI Engine Data Movement and Memory {Lecture}
- Versal AI engine tool flow {Lecture, Demo}
- The programming model {Lecture, Demo}

## TEACHING METHODS

- Inter-company online training :
  - Presentation by Webex by Cisco



- Provision of course material in PDF format

## SUPPORT

- Authorized Trainer Provider AMD : Engineer Electronics and Telecommunications ENSIL
  - Expert AMD FPGA - Language VHDL/Verilog - RTL Design
  - Expert AMD SoC & MPSoC - Language C/C++ - System Design
  - Expert DSP & AMD RFSoc - HLS - Matlab - Design DSP RF
  - Expert AMD Versal - AI Engines - Heterogenous System Architect

## PC RECOMMENDED

- Software Configuration :
  - WebEx Cisco

## PARTNERS



Authorized Training Provider

## CONTACT

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